

CLAIMS

What is claimed is:

1 1. An interconnection element comprising:
2 a first resilient element with a first contact region and a
3 second contact region and a first securing region; and
4 a second resilient element, with a third contact region and
5 a second securing region, coupled to the first resilient element
6 through respective securing regions and positioned such that
7 upon sufficient displacement of the first contact region toward
8 the second resilient element, the second contact region will
9 contact the third contact region,
10 wherein the interconnection element is of a size suitable
11 for connecting two electronic devices.

1 2. The interconnection element of claim 1, further comprising
2 an attachment element, wherein the second resilient element
3 comprises a third securing region and is coupled to the first
4 resilient element at the second securing region and the
5 attachment element at the third securing region.

1 3. The interconnection element of claim 1, comprising a
2 plurality of resilient elements coupled through respective
3 securing regions and selected such that collectively the

4 interconnection element has a spring constant determined by a
5 sum of the spring constants of the plurality of resilient
6 elements.

1 4. The interconnection element of claim 1, comprising a
2 plurality of resilient elements coupled through respective
3 securing regions, each one of the plurality of resilient
4 elements oriented to interact with an adjacent one of the
5 plurality of resilient elements upon sufficient displacement.

1 5. The interconnection element of claim 4, wherein the
2 plurality of resilient elements are of similar form and oriented
3 in an aligned relation, each resilient element adapted to
4 sequentially engage an adjacent resilient element upon
5 sufficient displacement such that the engaged resilient element
6 reinforces the displaced resilient element.

1 6. The interconnection element of claim 1, wherein each of the
2 resilient elements are of similar form.

1 7. The interconnection element of claim 6, wherein each of the
2 resilient elements comprises a rectangular beam, having a
3 length, a width, and a thickness.

1 8. The interconnection element of claim 7, wherein one of the
2 length and the thickness of the first resilient element is

3 different than the respective length and thickness of the second
4 resilient element.

1 9. The interconnection element of claim 7, wherein the length
2 and the thickness of the first resilient element is similar to
3 the respective length and thickness of the second resilient
4 element.

1 10. The interconnection element of claim 7, wherein the
2 thickness of at least one of the first resilient element and the
3 second resilient element is less than one mil (25 microns).

1 11. The interconnection element of claim 7, wherein at least
2 one of the first resilient element and the second resilient
3 element has a length of about 12 mils (300 microns) and a width
4 of about 3 mils (75 microns).

1 12. The interconnection element of claim 1, wherein the
2 interconnection element is of a size suitable for directly
3 contacting a semiconductor device.

1 13. The interconnection element of claim 1, wherein the
2 interconnection element is of a size suitable for directly
3 contacting a chip-scale device.

1 14. The interconnection element of claim 1, further comprising
2 a substrate, wherein the second resilient element comprises a
3 third securing region and is coupled to the first resilient
4 element at the second securing region and the substrate at the
5 third securing region.

1 15. The interconnection element of claim 14, wherein the
2 interconnection element is electrically coupled to the
3 substrate.

1 16. The interconnection element of claim 15, wherein the
2 substrate is one of a semiconductor, a ceramic, and a polymeric
3 substrate.

1 17. The interconnection element of claim 1, further comprising
2 a substrate, wherein the interconnection element is coupled to
3 the substrate together with a plurality of other interconnection
4 elements and the interconnection element is of a size suitable
5 for contacting a contact pad of a semiconductor device arranged
6 with a plurality of other contact pads at a pitch less than
7 about 10 mils.

1 18. The interconnection element of claim 1, wherein the contact
2 of the second contact region with the third contact region

3 comprises at least one of a mechanical and an electrical
4 contact.

1 19. An interconnection element comprising:

2 a first resilient element with a first contact region and a
3 second contact region and a first securing region; and

4 a second resilient element, with a third contact region and
5 a second securing region, coupled to the first resilient element
6 through respective securing regions and positioned such that
7 upon sufficient displacement of the first contact region toward
8 the second resilient element, the second contact region will
9 contact the third contact region,

10 wherein the interconnection element is capable of
11 connecting two electronic devices.

1 20. The interconnection element of claim 19, wherein upon
2 further displacement, the first contact region will displace the
3 second resilient element.

1 21. The interconnection element of claim 19, further comprising
2 an attachment element, wherein the second resilient element
3 comprises a third securing region and is coupled to the first
4 resilient element at the second securing region and the
5 attachment element at the third securing region.

1 22. The interconnection element of claim 19, comprising a
2 plurality of resilient elements coupled through respective
3 securing regions and selected such that collectively the
4 interconnection element has a spring constant determined by the
5 sum of the spring constants of the plurality of resilient
6 elements.

1 23. The interconnection element of claim 19, comprising a
2 plurality of resilient elements coupled through respective
3 securing regions, each one of the plurality of resilient
4 elements oriented to interact with an adjacent one of the
5 plurality of resilient elements upon sufficient displacement.

1 24. The interconnection element of claim 23, wherein the
2 plurality of resilient elements are of similar form and oriented
3 in an aligned relation, each resilient element adapted to
4 sequentially engage an adjacent resilient element upon
5 sufficient displacement such that the engaged resilient element
6 reinforces the displaced resilient element.

1 25. The interconnection element of claim 19, wherein each of
2 the resilient elements are of similar form.

1 26. The interconnection element of claim 25, wherein each of
2 the resilient elements comprises a rectangular beam, having a
3 length, a width, and a thickness.

1 27. The interconnection element of claim 26, wherein one of the
2 length and the thickness of the first resilient element is
3 different than the respective length and thickness of the second
4 resilient element.

1 28. The interconnection element of claim 26, wherein the length
2 and the thickness of the first resilient element is similar to
3 the respective length and thickness of the second resilient
4 element.

1 29. The interconnection element of claim 26, wherein the
2 thickness of at least one of the first resilient element and the
3 second resilient element is less than one mil (25 microns).

1 30. The interconnection element of claim 26, wherein at least
2 one of the first resilient element and the second resilient
3 element has a length of about 12 mils and a width of about 3
4 mils (75 microns).

1 31. The interconnection element of claim 19, wherein the
2 interconnection element is of a size suitable for directly
3 contacting a semiconductor device.

1 32. The interconnection element of claim 19, wherein the
2 interconnection element is of a size suitable for directly
3 contacting a chip-scale device.

1 33. The interconnection element of claim 19, further comprising
2 a substrate, wherein the second resilient element comprises a
3 third securing region and is coupled to the first resilient
4 element at the second securing region and the substrate at the
5 third securing region.

1 34. The interconnection element of claim 33, wherein the
2 interconnection element is electrically coupled to the
3 substrate.

1 35. The interconnection element of claim 34, wherein the
2 substrate is one of a semiconductor, a ceramic, and an organic
3 substrate.

1 36. The interconnection element of claim 19, further comprising
2 a substrate wherein the interconnection element is coupled to
3 the substrate together with a plurality of other interconnection

4 elements and the interconnection element is of a size suitable
5 for contacting a contact pad of a semiconductor device arranged
6 with a plurality of other contact pads at a pitch less than
7 about 10 mils.

1 37. An interconnection element comprising:

2 a first resilient element with a first contact region and a
3 second contact region and a first securing region; and

4 a second resilient element, with a third contact region and
5 a second securing region, coupled to the first resilient element
6 through respective securing regions and positioned such that
7 upon sufficient displacement of the first contact region toward
8 the second resilient element, the second contact region will
9 contact the third contact region,

10 wherein at least one of the first resilient element and the
11 second resilient element is formed by lithographic patterning of
12 material and deposition in a defined shape and of a size
13 suitable for connecting two electronic devices.

1 38. The interconnection element of claim 37, further comprising
2 an attachment element, wherein the second resilient element
3 comprises a third securing region and is coupled to the first
4 resilient element at the second securing region and the
5 attachment element at the third securing region.

1 39. The interconnection element of claim 37, comprising a
2 plurality of resilient elements coupled through respective
3 securing regions and selected such that collectively the
4 interconnection element has a spring constant determined by the
5 sum of the spring constants of the plurality of resilient
6 elements.

1 40. The interconnection element of claim 37, comprising a
2 plurality of resilient elements coupled through respective
3 securing regions, each one of the plurality of resilient
4 elements oriented to interact with an adjacent one of the
5 plurality of resilient elements upon sufficient displacement.

1 41. The interconnection element of claim 40, wherein the
2 plurality of resilient elements are of similar form and oriented
3 in an aligned relation, each resilient element adapted to
4 sequentially engage an adjacent resilient element upon
5 sufficient displacement such that the engaged resilient element
6 reinforces the displaced resilient element.

1 42. The interconnection element of claim 37, wherein each of
2 the resilient elements are of similar form.

1 43. The interconnection element of claim 42, wherein each of
2 the resilient elements comprises a rectangular beam, having a
3 length, a width, and a thickness.

1 44. The interconnection element of claim 43, wherein one of the
2 length and the thickness of the first resilient element is
3 different than the respective length and thickness of the second
4 resilient element.

1 45. The interconnection element of claim 43, wherein the length
2 and the thickness of the first resilient element is similar to
3 the respective length and thickness of the second resilient
4 element.

1 46. The interconnection element of claim 43, wherein the
2 thickness of at least one of the first resilient element and the
3 second resilient element is less than one mil (25 microns).

1 47. The interconnection element of claim 43, wherein at least
2 one of the first resilient element and the second resilient
3 element has a length of 12 mils (300 microns) and a width of 3
4 mils (75 microns).

1 48. The interconnection element of claim 37, wherein the
2 interconnection element is of a size suitable for directly
3 contacting a semiconductor device.

1 49. The interconnection element of claim 37, wherein the
2 interconnection element is of a size suitable for directly
3 contacting a chip-scale device.

1 50. The interconnection element of claim 37, further comprising
2 a substrate, wherein the second resilient element comprises a
3 third securing region and is coupled to the first resilient
4 element at the second securing region and the substrate at the
5 third securing region.

1 51. The interconnection element of claim 50, wherein the
2 interconnection element is electrically coupled to the
3 substrate.

1 52. The interconnection element of claim 51, wherein the
2 substrate is one of a semiconductor, a ceramic, and an organic
3 substrate.

1 53. The interconnection element of claim 37, further comprising
2 a substrate, wherein the interconnection element is coupled to
3 the substrate together with a plurality of other interconnection

4 elements and the interconnection element is of a size suitable
5 for contacting a contact pad of a semiconductor device arranged
6 with a plurality of other contact pads at a pitch less than
7 about 10 mils (250 microns).

1 54. An interconnection element comprising:

2 a body comprising a plurality of resilient elements, a
3 first resilient element defining a first surface of the body and
4 a surface of a second resilient element defining a second
5 surface of the body; and

6 an attachment element coupled to one of the first surface
7 and the second surface of the body and of a size suitable for
8 coupling to a contact region of an electronic device, and

9 wherein one of the plurality of resilient elements is
10 deformed it engages another one of the plurality of resilient
11 elements which reinforces the engaging resilient element.

1 55. The interconnection element of claim 54, wherein the
2 plurality of resilient elements are coupled through respective
3 securing regions and selected such that collectively the
4 resilient elements define a predetermined spring constant.

1 56. The interconnection element of claim 54, wherein the
2 plurality of resilient elements are of similar form and oriented
3 in an aligned relation.

1 57. The interconnection element of claim 56, wherein the
2 resilient elements comprise a rectangular beam, having a length,
3 a width, and a thickness.

1 58. The interconnection element of claim 57, wherein one of the
2 length and the thickness of the first resilient element is
3 different than the respective length and thickness of the second
4 resilient element.

1 59. The interconnection element of claim 57, wherein the
2 thickness of at least one of the first resilient element and the
3 second resilient element is less than one mil (25 microns).

1 60. The interconnection element of claim 57, wherein at least
2 one of the plurality of resilient elements has a length of about
3 12 mils (300 microns) and a width of about 3 mils (75 microns).

1 61. The interconnection element of claim 56, wherein the first
2 resilient element comprises a portion of cylinder.

1 62. The interconnection element of claim 54, wherein the
2 interconnection element is electrically coupled to a substrate.

1 63. The interconnection element of claim 62, wherein the
2 substrate is one of a semiconductor, a ceramic, and a polymeric
3 substrate.

1 64. The interconnection element of claim 54, further comprising
2 a substrate, wherein the interconnection element is coupled to
3 the substrate together with a plurality of other interconnection
4 elements and the interconnection element is of a size suitable
5 for contacting a contact pad of a semiconductor device arranged
6 with a plurality of other contact pads at a pitch less than 10
7 mils (250 microns).

1 65. The interconnection element of claim 54, wherein the
2 interconnection element is of a size suitable for directly
3 contacting a semiconductor device.

1 66. The interconnection element of claim 54, wherein the
2 interconnection element is of a size suitable for directly
3 contacting a chip-scale device.

1 67. An electronic assembly comprising:
2 a plurality of interconnection elements coupled to a
3 substrate and configured in a relation to contact an array of
4 contact pads of an electronic device, each interconnection
5 element comprising:

6 a first resilient element with a first contact region and a
7 second contact region and a first securing region; and
8 a second resilient element with a third contact region and
9 a second securing region coupled to the first resilient element
10 through respective securing regions and positioned such that
11 upon sufficient displacement of the first contact region towards
12 the second resilient element, the second contact region will
13 contact the third contact region.

1 68. The electronic assembly of claim 67, wherein the plurality
2 of interconnection elements comprise a plurality of resilient
3 elements coupled through respective securing regions and
4 selected such that collectively each of the interconnection
5 elements has a predetermined spring constant determined by the
6 sum of the spring constants of the plurality of the resilient
7 elements.

1 69. The electronic assembly of claim 68, wherein the resilient
2 elements of each of the plurality of interconnection elements
3 are of similar form and oriented in an aligned relation.

1 70. The electronic assembly of claim 69, wherein the resilient
2 elements comprise a rectangular beam, having a length, a width,
3 and a thickness.

1 71. The electronic assembly of claim 67, wherein the substrate
2 is one of a semiconductor, a ceramic, and an organic substrate.

1 72. The electronic assembly of claim 67, wherein the
2 interconnection elements are of a size suitable for contacting a
3 contact pad of a semiconductor device arranged with a plurality
4 of other contact pads at a pitch less than about 10 mils (250
5 microns).

1 73. The electronic assembly of claim 67, wherein the
2 interconnection elements are of a size suitable for contacting
3 an array of contact pads on a semiconductor device.

1 74. The electronic assembly of claim 67, wherein the
2 interconnection elements are of a size suitable for contacting
3 an array of contact pads of a chip-scale device.

1 75. The electronic assembly of claim 67, wherein a first
2 interconnection element and a second interconnection element are
3 arranged on the substrate such that upon sufficient displacement
4 of the first contact region of the first interconnection
5 element, the first interconnection element contacts the second
6 interconnection element.

1 76. The electronic assembly of claim 75, wherein the contact of
2 the first interconnection element and the second interconnection
3 element is one of an electrical and a mechanical contact.

1 77. An interconnection element comprising:
2 a first resilient element having a contact point;
3 a second resilient element; and
4 an attachment element that defines a space between the
5 first resilient element and the second resilient element,
6 wherein a first projection through the attachment element
7 is parallel to a second projection between the first resilient
8 element and the second resilient element and to a third
9 projection between the first resilient element and the second
10 resilient element and a vector between the first projection and
11 the second projection has a direction different than a vector in
12 the same plane between the first projection and the third
13 projection, and
14 wherein upon the application of sufficient force on the
15 contact point, each of the first resilient element and the
16 second resilient element will deform.

1 78. The interconnection element of claim 77, wherein each of
2 the resilient elements are of similar form.

1 79. The interconnection element of claim 78, wherein each of
2 the resilient elements comprises a portion of a circle.

1 80. The interconnection element of claim 77, wherein at least
2 one of the resilient elements has an opening therethrough.

1 81. The interconnection element of claim 77, wherein at least
2 one of the resilient elements comprises one of a clover shape, a
3 rectangular shape, and an H-shape.

1 82. The interconnection element of claim 77, wherein the
2 interconnection element is of a size suitable for directly
3 contacting a semiconductor device.

1 83. The interconnection element of claim 77, wherein the
2 interconnection element is of a size suitable for directly
3 contacting a chip-scale device.